



PATENT

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): David Dice, Mark S. Moir and William N. Scherer III

Title: QUICKLY REACQUIRABLE LOCKS

Application No.: 10/669,948 Filed: September 24, 2003

Examiner: Unassigned Group Art Unit: 2185

Atty. Docket No.: 004-8728

July 16, 2004

Mail Stop Amendment
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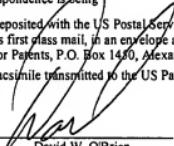
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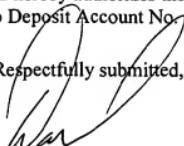
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Date
7/16/04

Respectfully submitted,



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U.S. Department of Commerce, Patent and Trademark Office		Attorney Docket No.: 004-8728
INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Use several sheets if necessary)		Application No.: 10/669,948
JUL 19 2004		Applicant(s): Dice et al.
		Filing Date: September 24, 2003
		Group Art Unit: 2185
		Date Submitted: July 16, 2004
NON PATENT LITERATURE DOCUMENTS		
*Examiner Initial	TRADE Initial	(Including name of author in capital letters, title of article, title of item, date, pertinent pages, volume-issue number(s), publisher, city and/or country where published.)
✓ /CA/	3	Ageson, Ole et al., "An Efficient Meta-Lock for Implementing Ubiquitous Synchronization", ACM SIGPLAN Notices, Vol. 34, No. 10, pages 207-222, 1999.
✓	2	Bacon, David F. et al., "Thin Locks: Featherweight Synchronization for Java", ACM SIGPLAN Notices, Vol. 33, No. 6, pages 258-268, 1998.
✓	3	Dice, David, "Implementing Fast Java™ Monitors with Relaxed Locks", In Proceedings of the USENIX JVM'01 Conference, 2001.
✓ /CA/	3	Lamport, Leslie, "A Fast Mutual Exclusion Algorithm", ACM Transactions on Computer Systems, Vol. 5, No. 1, pages 1-11, 1987.
Examiner /Charles Anya/		Date Considered 06/13/2009
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